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<td>Author(s)</td>
<td>Hariya, Akinori; Matsuura, Ken; Yanagi, Hiroshige; Tomioka, Satoshi; Ishizuka, Yoichi; Ninomiya, Tamotsu</td>
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5MHz PWM-Controlled Current-Mode Resonant DC-DC Step-Down Converter Using GaN-HEMTs

Akinori Hariya  
Student Member, IEEE  
Nagasaki University  
1-14 Bunkyo-machi, Nagasaki-shi, Nagasaki, Japan  
bb52312202@ec.nagasaki-u.ac.jp

Ken Matsuura  
TDK Corporation  
2-15-7 Higashiwada, Ichikawa-shi, Chiba, Japan  
matsuen@jp.tdk-lambda.com

Hiroshige Yanagi  
TDK-Lambda Corporation  
2704-1 Settaya-machi, Nagaoka-shi, Nigata, Japan  
yanagi@jp.tdk-lambda.com

Satoshi Tomioka  
TDK-Lambda Corporation  
2704-1 Settaya-machi, Nagaoka-shi, Nigata, Japan  
s.tomioka@jp.tdk-lambda.com

Yoichi Ishizuka  
Member, IEEE  
Nagasaki University  
1-14 Bunkyo-machi, Nagasaki-shi, Nagasaki, Japan  
isy2@nagasaki-u.ac.jp

Tamotsu Ninomiya  
Fellow, IEEE  
The International Centre for the Study of East Asian Development  
1-8 Hibikino, Wakamatsu-ku, Kitakyushu-shi, Fukuoka, Japan  
t_ninomiya@icsead.or.jp

Abstract — High power efficiency and high power density are required in regulated isolated DC-DC converters. In this paper, a novel pulse width modulation (PWM) control method which is suitable for the isolated current-mode resonant DC-DC converter operated at MHz level switching frequency is proposed. The output voltage with the proposed method can be regulated with no additional components at fixed switching frequency. In addition, zero voltage switching (ZVS) of primary-side switches at turn-on can be maintained. The principle of the proposed method and the method of ZVS operation in the proposed method are explained.

Some experiments have been performed with a 5MHz isolated step-down DC-DC converter using Gallium Nitride High Electron Mobility Transistors (GaN-HEMTs); the output voltage is 12V and the total volume of the circuit is 16.14cm³. With the proposed PWM control method, input voltage range is 42-45.5V, and maximum load current range is 10A at \( V_i = 45.5V \). The ZVS of the primary-side switches at turn-on is confirmed in all experimental regions, and the maximum power efficiency is 89.2%.

Index Terms— DC-DC power converters, Gallium nitride, Pulse width modulation converters, Switching converters, Zero voltage switching.

I. INTRODUCTION

Recently, high power efficiency and high power density power conversion systems have been required in information and communication technology (ICT) equipment due to increases in system power consumption. Therefore, intermediate bus architectures (IBAs) have been introduced in this field in recent years [1]. The performances of three basic IBAs are investigated in [2]. The literature reported that the double regulated IBA has the potential of the best performance. This architecture is composed of a regulated bus converter and regulated point of load (POL) converters. An isolated step-down DC-DC converter is used as the bus converter in IBA. The rated input voltage is 48V, and the output target voltage is 12V which is the intermediate bus voltage. At the next stage of bus conversion, 12V is regulated in POLs to e.g. 2.5 or 1.0V required by each load. In this architecture, the design of the regulated bus converter is especially difficult due to requirements both of isolation and regulation. Thus, research of regulated bus converters is important for more improvement of the double regulated IBA. In this paper, a 5MHz PWM-controlled current-mode resonant DC-DC step-down converter for regulated bus converters is discussed.

Corresponding to the requirement of high power density, the increase in the switching frequency of these converters has been considered to be one of the key approaches. Therefore, a 5MHz switching frequency, which is relatively high for isolated DC-DC converters, is adopted in this research [3]-[5]. However, by adopting a high switching frequency, power loss such as switching loss and gate driving loss increases. To solve this problem, the current-mode resonant DC-DC converter is used because this converter can reduce switching loss. Also, GaN-HEMTs are suitable for high switching frequency operation as semiconductor switches because of low gate driving loss.

The output voltages of current-mode resonant DC-DC converters are usually controlled by pulse frequency modulation (PFM). However, PFM controlled current-mode resonant DC-DC converter is not suitable for miniaturization. The details are described in section III.

The novel PWM control method which is suitable for the isolated current-mode resonant DC-DC converter operated at MHz level switching frequency is proposed. This converter topology is the same as the conventional current-mode resonant DC-DC converter with synchronous rectification. In general, when load current increases or input voltage decreases from rated input voltage, the output voltage is decreased. Therefore, to maintain constant output voltage, boost conversion is needed. The proposed PWM control method can regulate output voltage without any additional
components. By using the transformer’s leakage inductance and secondary-side synchronously rectifying switches, secondary-side boost conversion operation can be realized. In MHz level switching operation for high power density, switching loss increases dramatically. To suppress switching loss, ZVS operation is needed. The proposed PWM control method can maintain ZVS of primary-side switches at the turn-on in all operating regions.

The targets of the research are to obtain the high performances which are small volume, 42-53V input voltage range, which is used in information servers and other computer applications [6], 10A of maximum load current range, and the realization of ZVS of primary-side switches at turn-on.

In section II, the approach of the realization of DC-DC converter operated at MHz level switching frequency is described. In section III, the issue of the conventional PFM-controlled current-mode resonant DC-DC converter is revealed. In section IV, the proposed PWM-controlled current-mode resonant DC-DC converter is explained. In section V, the experimental results are demonstrated.

II. THE APPROACH OF THE REALIZATION OF DC-DC CONVERTER OPERATED AT MHZ LEVEL SWITCHING FREQUENCY

For miniaturization of the DC-DC converter, the increase in the switching frequency is considered to be one of the key approaches.

As shown in Fig. 1, there are not many reports in the literature for the region of over 5MHz switching frequency and 100W output power with more than 90% of power efficiency. Therefore, this research challenges 5MHz of switching frequency at 120W of output power. To suppress increasing core power loss with high switching frequency, NiZn ferrite core is used [18]. To increase power density, a multilayer printed circuit board (PCB) is employed in this research. The details of PCB layout technique is shown in [4]. Also, to increase more power density, a planar transformer is employed in the research. The planar transformer is directly manufactured with multi-layered pattern in the PCB.

In the prototype circuit, conventional hard-switched gate driving is used for the GaN-HEMTs. Therefore, the gate driving loss $P_d$ is expressed by

$$P_d = Q_g V_{GS} f_s$$  \hspace{1cm} (1)

where $Q_g$ is gate charge, $V_{GS}$ is gate-to-source voltage of GaN-HEMT, and $f_s$ is switching frequency. From eq. (1), this power loss is proportional to $f_s$.

For reducing gate driving loss, GaN-HEMTs are adopted in this research because GaN-HEMTs have low $Q_g$ and be driven at low gate-to-source voltage. The benefits of GaN-HEMTs are not purely low $Q_g$, but a physically smaller transistor for a given low drain-to-source on resistance $R_{DS(on)}$ and $V_{gs}$ blocking capability. Due to physically smaller device with lower parasitic capacitances, faster device switching speed results. Additionally, GaN-HEMTs have no source-to-drain recovery charge [19] and low package inductance because of small package size [20]. Other researches have validated the effectiveness of GaN-HEMTs for high switching frequency DC-DC converters [7]-[10], [14]. Also, GaN-HEMTs have been proved more effective than silicon devices in [7], [9]-[11], [13].

However, because of GaN-HEMTs having low gate-to-source threshold voltage $V_{th}$, low maximum gate-to-source voltage $V_{GS}$, and high source-to-drain voltage $V_{SD}$, it is difficult to drive GaN-HEMTs. Therefore, suitable drive circuit for GaN-HEMTs is needed. Some literatures show that driver LM5113 is suitable for driving GaN-HEMTs [21].

By adopting a high switching frequency, switching loss increases dramatically. However, if switches can achieve soft-switching such as ZVS operation at the turn-on, the switching loss can be suppressed to almost zero. In this research, current-mode resonant DC-DC converter topology featuring ZVS of primary-side switches at turn-on is adopted. Many studies of this circuit topology in MHz level switching operation have been done [7], [10], [14].

III. THE ISSUE OF THE CONVENTIONAL PFM-CONTROLLED CURRENT-MODE RESONANT DC-DC CONVERTER

To reduce the volume of the DC-DC converter, the resonant inductance of the converter needs to be minimized. For effective miniaturization of the converter, the miniaturization of resonant inductance is important. Meanwhile, to reduce the switching loss, the current-mode resonant DC-DC converter is widely used [7], [10], [12]-[17], [22], [23]. Generally, the current-mode resonant DC-DC converter is controlled by PFM control which varies switching frequency. In Fig. 2, the relation of resonant inductance and switching frequency is shown. $L_r=100nH$ and $L_r=10nH$ are in Fig. 2 (a) and (b), respectively. The other parameters are $V_i=48V$, $n=2.2$, $L_m=200nH$.

In this figure, the definitions of the converter parameters are

$$M = (2nV_o)\left|V_i\right|,$$  \hspace{1cm} (2)
The influence of \( L_r \) and \( L_m \) can be observed from Fig. 2 (a) and (b). In case of \( L_r = 100 \text{nH} \), for realizing the range, \( F \) is changed from 0.81 to 1. In contrast, in case of \( L_r = 10 \text{nH} \), for realization of the range, \( F \) is changed from 0.43 to 1.

If large resonant inductance value is used, the region of switching frequency is narrow, which leads to small size noise filter. However, to obtain large resonant inductance value, the physical size of the inductor becomes large. If small resonant inductance value is used, the region of switching frequency is wide, which leads to large size noise filter. There is a trade-off between the size of the required noise filter and resonant inductor.

As the result, for the miniaturization of the DC-DC converter, it is shown that pure PFM control is not appropriate. Therefore, PWM-controlled current-mode resonant DC-DC conversion has been proposed [3], [5].

IV. THE PROPOSED PWM-CONTROLLED CURRENT-MODE RESONANT DC-DC CONVERTER

In the previous researches, some PWM-controlled current-mode resonant DC-DC converters have been presented [24]-[26]. For example, the method of additional auxiliary circuits for regulating output voltage [24], [25], and the method of controlling the duty ratio of primary-side switches [26] have been proposed. However, these methods need some additional components for regulating output voltage. On the other hand, the output voltage of the proposed current-mode resonant DC-DC converter can be controlled without any additional components at fixed switching frequency. In addition, ZVS of primary-side switches at turn-on can be accomplished in the proposed method. In this section, the principle of the proposed method and the method for achieving ZVS turn-on are explained.

\[
\kappa = \frac{L_m}{L_r} ,
\]

\[
F = \frac{f_s}{f_0} ,
\]

\[
f_0 = \frac{1}{2\pi\sqrt{L_r C_r}} ,
\]

\[
\omega_0 = 2\pi f_0 ,
\]

\[
Q = \frac{Z_0}{R_{ac}} ,
\]

\[
R_{ac} = \frac{8n^2 R_L}{\pi^2}
\]

and

\[
Z_0 = \sqrt{L_r/C_r}
\]

where turn ratio \( n \), magnetizing inductance \( L_m \), resonant inductance \( L_r \), switching frequency \( f_s \), resonant capacitance \( C_r \) and load resistance \( R_L \). According to [22], eq. (2) can be converted from eqs. (3) to (8) as (10). Eq. (10) can be

\[
M = \frac{1}{1 + \frac{1}{\kappa} \left( 1 - \frac{1}{F^2} \right)^2 + Q^2 \left( 1 - \frac{1}{F} \right)^2}
\]

It is assumed that input range voltage is from 42 to 53V at \( R_L = 1.2\Omega \) and \( V_o = 12V \). The influence of \( L_r \) on \( M \) can be confirmed from Fig. 2 (a) and (b). In case of \( L_r = 100\text{nH} \), for realization of the range, \( F \) is changed from 0.81 to 1. In contrast, in case of \( L_r = 10\text{nH} \), for realization of the range, \( F \) is changed from 0.43 to 1.

The circuit topology used in this paper.

A. The Circuit Topology

The circuit topology is based on a half-bridge type current-mode resonant DC-DC converter as shown in Fig. 3. The primary-side is the half-bridge topology. \( Q_1 \) and \( Q_2 \) are driven in 50% duty ratio, alternatively. \( C_{1r} \) and \( C_{2r} \) are the resonant capacitors which have the same capacitance and also make averaged voltage of \( V_r \) to one half of the input. The inside of the broken line is the equivalent circuit of the magnetic transformer, where \( L_r \) is leakage inductance, and \( L_m \) is the transformer’s magnetizing inductance. The turn ratio is \( n : 1 \). \( L_r \) is used as the resonant inductance. The secondary-side is the full-bridge topology composed with diodes \( D_1 \) and \( D_2 \) for high-side arm switches, and transistors \( Q_3 \) and \( Q_4 \) for low-side arm switches.

B. The Principle of the Proposed PWM Control Method

To simplify analysis of the circuit operation, the following assumptions are made:

- GaN-HEMTs are treated as ideal switches;
- The reverse-conduction voltage drops of the primary-side switches are neglected;
Because of the gate is shorted to the source by the gate drivers during off-term, drain-to-gate capacitance therefore appear in parallel with drain-to-source capacitance of each GaN-HEMTs. Therefore, the capacitances of the primary-side GaN-HEMTs which labeled \( C_{oss1} \) and \( C_{oss2} \) are defined as the sum of the drain-to-gate capacitance and the drain-to-source capacitance during off-term, respectively. These capacitances are assumed as \( C_{oss} = C_{oss1} + C_{oss2} \):

- Resonant capacitances are satisfied \( C_r = C_{r1} + C_{r2} \):
- The forward voltage drops and the parasitic capacitances of the secondary-side diodes are neglected;
- The output capacitances and the reverse-conduction voltage drops of the secondary-side switches are neglected;
- The output voltage is constant;

The output voltage can be controlled by changing the duty ratio of \( Q_3 \) and \( Q_4 \), simultaneously. When the duty ratio is less than 0.5, the circuit operates as well as conventional current resonance circuit. When the duty ratio is more than 0.5, the circuit operates in the proposed operation.

The operational waveforms are shown in Fig. 4. The circuit operation is separated into 10 states in the proposed method. First half switching cycle is from state 1 to 5. Second half switching cycle is the remaining states. The second half switching cycle is symmetrical in operation to the first half.

![Operational waveforms](image)

**Fig. 4. The operational waveforms of the proposed PWM control.**

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switching cycle. Therefore, because same analytical results can be obtained in each switching cycle, the description for the second half switching cycle is omitted. The switch combination is shown in TABLE I. The equivalent circuits for each state of a half switching term are shown in Fig. 5. In this figure, the switches drawn with weak colors represent OFF, and red line represents current flow.

The definitions of the duty ratio $D$ are followed as

$$D = \frac{T_{on}}{T_s}, \quad (11)$$

$$D = \frac{1}{2} + D_1 + D_4 + D_5, \quad (12)$$

$$D_1 + D_2 + D_3 + D_4 + D_5 = \frac{1}{2} \quad (13)$$

and

$$\begin{cases} D \leq 0.5 \quad \text{…………(conventional operation)} \\ D > 0.5 \quad \text{…………(proposed operation)} \end{cases} \quad (14)$$

where $T_s$ is the switching period, and $T_{on}$ is the on-term of Q3 and Q4. $D_1\sim D_5$ are the duty ratio of each state. The definitions of the resonant operation are as follows;

$$\omega_1 = \frac{1}{\sqrt{L_C + L_{m}}}, \quad (15)$$

$$Z_1 = \sqrt{L_C + L_{m}} / C_r \quad (16)$$

and

$$\omega_{oxs} = \frac{1}{\sqrt{L_r C_{oxs}}} \quad (17)$$

The definitions of the initial value of the variables are as follows;

$$v_{c1}(0) = V_i/2 - V_c \quad (18)$$

and

$$i_{r1}(0) = I_r \quad (19)$$

The descriptions for each state are described below.

State 1 ($0 < t < D_1 T_s)$;

In this state, $t_1$ is defined as $t_1 = t$. The primary-side switch Q1 is turned ON. Also, the secondary-side switches both Q2 and Q4 are turned ON. Q1 and Q4 are overlapped as indicated with the light gray area in Fig. 4. The resonant inductance $L_r$ is magnetized by $i_r$ for boosting output voltage. From the figure, $v_{c1}(t)$ and $i_{r1}(t)$ become

$$v_{c1}(t_1) = V_i - \left( V_i / 2 + V_c \right) \cos(\omega_0 t_1) + Z_0 I_r \sin(\omega_0 t_1) \quad (20)$$

and

$$i_{r1}(t_1) = - \left[ Z_0 (V_i / 2 + V_c) \sin(\omega_0 t_1) + I_r \cos(\omega_0 t_1) \right] \quad (21)$$

The final values of the state 1 are

$$V_{c2} = v_{c1}(D_1 T_s) \quad (22)$$

and

$$I_{r2} = i_{r1}(D_1 T_s). \quad (23)$$

State 2 ($D_1 T_s < t < (D_1 + D_2) T_s)$;

In this state, $t_2$ is defined as $t_2 = t - D_1 T_s$. After Q4 is turned OFF, the direction of the voltage applied to D2 is inverted, and diode of D2 becomes ON. The inductance current which is magnetized in state 1 flow through diode D2 and switch Q1, to the load. From the figure, $v_{c2}(t)$ and $i_{r2}(t)$ become

$$v_{c2}(t_2) = V_i - n V_o \quad (24)$$

and

$$i_{r2}(t_2) = 1 / Z_0 \left( V_{c2} - V_i + n V_o \right) \sin(\omega_0 t_2) + I_{r2} \cos(\omega_0 t_2) \quad (25)$$

The final values of the state 2 are

$$V_{c3} = v_{c2}(D_2 T_s) \quad (26)$$

and

$$I_{r3} = i_{r2}(D_2 T_s). \quad (27)$$

State 3 (($D_1 + D_2) T_s < t < (D_1 + D_2 + D_3) T_s)$:

In this state, $t_3$ is defined as $t_3 = t - D_1 T_s - D_2 T_s$. The direction of the diode D2 current is inverted, and diode of D2 becomes OFF. In the state, resonant current $i_r$ equal to magnetizing current $i_{m}$. From the figure, $v_{c3}(t_3)$ and $i_{r3}(t_3)$ become

$$v_{c3}(t_3) = V_i + (V_{c3} - V_i) \cos(\omega_0 t_3) - Z_0 I_{r3} \sin(\omega_0 t_3) \quad (28)$$

and

$$i_{r3}(t_3) = 1 / Z_0 \left( V_{c3} - V_i \right) \sin(\omega_0 t_3) + I_{r3} \cos(\omega_0 t_3). \quad (29)$$

The final values of the state 3 are

$$V_{c4} = v_{c3}(D_3 T_s) \quad (30)$$

and

$$I_{r4} = i_{r3}(D_3 T_s). \quad (31)$$

State 4 (($D_1 + D_2 + D_3) T_s < t < (D_1 + D_2 + D_3 + D_4) T_s)$:

In this state, $t_4$ is defined as $t_4 = t - D_1 T_s - D_2 T_s - D_3 T_s$. This state is similar to state 1. In the state, the resonant inductance $L_s$ is magnetized by $i_r$ for ZVS of the primary-side switch at turn-on. Q3 and Q4 are overlapped as indicated with the dark gray area in Fig. 4. From the figure, $v_{c4}(t_4)$ and $i_{r4}(t_4)$ become

$$v_{c4}(t_4) = V_i + (V_{c4} - V_i) \cos(\omega_0 t_4) - Z_0 I_{r4} \sin(\omega_0 t_4) \quad (32)$$

and

$$i_{r4}(t_4) = 1 / Z_0 \left( V_{c4} - V_i \right) \sin(\omega_0 t_4) + I_{r4} \cos(\omega_0 t_4). \quad (33)$$

The final values of the state 4 are

$$V_{c5} = v_{c4}(D_4 T_s) \quad (34)$$

and

$$I_{r5} = i_{r4}(D_4 T_s). \quad (35)$$

State 5 (($D_1 + D_2 + D_3 + D_4) T_s < t < T_s/2)$:

In this state, $t_5$ is defined as $t_5 = t - D_1 T_s - D_2 T_s - D_3 T_s - D_4 T_s$. The primary-side switch Q1 is turned OFF. All primary-side switches are turned OFF, called dead time. The parasitic capacitor $C_{oxs}$ of Q1 is discharged by a half of resonant inductance current $i_r$. Q3 and Q4 are overlapped as indicated with the dark gray area in Fig. 4. From the figure, $v_{c5}(t_5)$, $V_{oxs}$ (t5) and $i_{r5}(t_5)$ become

$$v_{c5}(t_5) = V_i - \frac{\omega_0^2 (V_{c5} - V_i)}{2} \frac{(V_{c5} - V_i)}{2} \cos^2 \left( \frac{\sqrt{\omega_0^2 + \omega_{oxs}^2} \cdot t_5}{2} \right) \quad (36)$$

and

$$i_{r5} = \frac{I_{r5}}{C_{oxs} \sqrt{\omega_0^2 + \omega_{oxs}^2}} \sin \left( \frac{\sqrt{\omega_0^2 + \omega_{oxs}^2} \cdot t_5}{2} \right) \quad (37)$$
\[ v_{DS5}(t_5) = \frac{\omega_{oss}^2(V_{c5} - V_i)}{\omega_0 + \omega_{oss}^2} + V_i \]
\[ - \frac{\omega_{oss}^2(V_{c5} - V_i)}{\omega_0 + \omega_{oss}^2} \cos \left( \sqrt{\omega_0^2 + \omega_{oss}^2} \cdot t_5 \right) \]
\[ + \frac{I_{r5}}{C_{oss} \sqrt{\omega_0^2 + \omega_{oss}^2}} \sin \left( \sqrt{\omega_0^2 + \omega_{oss}^2} \cdot t_5 \right) \]
\[ \text{and} \]
\[ i_{r5}(t_5) = \frac{V_{c5} - V_i}{L_r \omega_0^2 + \omega_{oss}^2} \cdot \sin \left( \sqrt{\omega_0^2 + \omega_{oss}^2} \cdot t_5 \right) \]
\[ + I_{r5} \cos \left( \sqrt{\omega_0^2 + \omega_{oss}^2} \cdot t_5 \right) \]

(37)

(38)

C. The Method for Achieving ZVS of Primary-side Switches at Turn-On in the Proposed PWM Control

To accomplish ZVS of the primary-side switches at turn-on, small magnetizing inductances \( L_m \) have been used in current-mode resonant DC-DC converters generally. However, small \( L_m \) value leads to increase of primary-side current which can cause a reduction of power efficiency. Therefore, to accomplish ZVS operation in the proposed method, phase-shift between primary and secondary-side switches which controls resonant current is proposed.

As shown in Fig. 4, the proposed operation has phase-shift between primary and secondary-side switches. \( \Delta t_{ps} \) is the time length of the phase-shift. Without the phase-shift, \( \Delta t_{ps} = 0 \)ns, the previous state of the dead time becomes discontinuous current state. With this situation, initial current cannot be charged enough for ZVS operation because of secondary-side parasitic capacitances. Therefore, even with the long dead time duration, ZVS cannot be achieved. With the phase-shift, the problem of the initial current can be solved. From eqs. (33) and (35), the initial current of the dead time, \( I_{r5} \) is
\[ I_{r5} = \frac{1}{2} Z_0 |W_{c4} - V_i| \sin(\omega_0 D_4 T_5) + I_{r4} \cos(\omega_0 D_4 T_5). \]

(39)

The term of the phase-shift becomes
\[ \Delta t_{ps} = D_4 T_5 + D_4 T_5. \]

(40)

In eq. (40), as defined as the \( D_4 T_5 \) is fixed, the term of state 4 becomes larger with the increase of \( \Delta t_{ps} \). \( I_{r5} \) is related to \( \Delta t_{ps} \) almost linearly. As \( I_{r5} \) increases, the amount of electrical charge \( q_{r5} \) which flows in the resonant inductance during dead time becomes larger.

Meanwhile, the output capacitances of GaN-HEMTs are not constant. The value of output capacitance depends on drain-to-source voltage. The influence of drain-to-source voltage on output capacitance is confirmed in [19], as shown in Fig. 6. In considerations of this influence, the initial electrical charge of dead time \( q_{oss} \) is expressed by
\[ q_{oss} = \int_0^V C_{oss} (v_{DS}) \cdot dv_{DS} \]

(41)

From eq. (41), the influence of drain-to-source voltage on \( q_{oss} \) can be obtained, as shown in Fig. 6.

For an example, when input voltage is 42V, \( q_{oss1} \) and \( q_{oss2} \)
are 32.1nC from Fig. 6. Therefore, equivalent output capacitances $C_{oss1}$ and $C_{oss2}$ are 764pF, respectively. In the conditions of $C_{oss} = 1528pF$, $V_i = 42V$, $R_L = 20ohm$, $D_iT_i$ is 10ns, the relation of $\Delta \mu$ and $q_{s5}$ are shown in Fig. 7. The electrical charge $q_{ss}$ can be calculated with

$$q_{ss} = \int_{0}^{D_iT_i} i_{ss}(t)dt$$

(42)

For achieving ZVS, $q_{ss}$ has to be larger than the amount of the electrical charge of the output capacitance of the switches as

$$|q_{ss}| > q_{ass}$$

(43)

In this case, due to $q_{ass} = 64.2nC$, if $q_{ss}$ is larger than 64.2nC, ZVS operation can be achieved. From the figure, it can be seen that the phase-shift $\Delta \mu$ between primary and secondary-side switches are valid for ZVS operation in the proposed PWM control.

D. The Static Characteristics of the Proposed Method at MHz Level Operation

Similar to section III, two examples of the static characteristics of the proposed PWM controlled current-mode resonant DC-DC converter are shown in Fig. 8. $L_r = 100nH$ and $L_s = 10nH$ are in Fig. 2 (a) and (b), respectively. The other parameters are $V_i = 48V$, $n = 2.2$, $L_m = 200nH$.

It is assumed that input voltage range is from 42 to 53V at $R_L = 1.2\Omega$ and $V_o = 12V$. In case of $L_r = 100nH$, for realization of the range, $D$ is changed from 0.5 to 0.605. In contrast, in case of $L_s = 10nH$, for realization of the range, $D$ is changed from 0.5 to 0.55. By comparing between two parameters, the case of $L_s = 10nH$ can control in narrow duty ratio than the other one. Therefore, the proposed PWM control method is seen to be suitable for the miniaturization of the DC-DC converter because the output voltage can be controlled with small resonant inductance.

V. EXPERIMENTAL RESULTS

In this section, it can be confirmed the difference between the experimental results of the proposed method and the targets of the research.

A. The Experimental Conditions

As a prototype digital controller, a field programmable gate array (FPGA) Cyclone IV is used, which generates individual gate signal for each switch. The on-terms of the gate signals are manually changed with software. The resolution of the gate signals is 1 nano second.

Some experiments have been performed with parameters as shown in TABLE II. Components used in the experiment are shown in TABLE III.

The prototype 5MHz isolated DC-DC converter with GaN-HEMTs is shown in Fig. 9. The total volume of the prototype circuit is 16.14cm$^3$.

<table>
<thead>
<tr>
<th>TABLE II. EXPERIMENTAL PARAMETERS</th>
</tr>
</thead>
<tbody>
<tr>
<td>Specifications</td>
</tr>
<tr>
<td>Input voltage: $V_i$</td>
</tr>
<tr>
<td>Transformer ratio $n : 1$</td>
</tr>
<tr>
<td>Switching frequency: $f_s$</td>
</tr>
<tr>
<td>Transformer leakage inductance: $L_m$</td>
</tr>
<tr>
<td>Transformer magnetizing inductance: $L_s$</td>
</tr>
<tr>
<td>Resonant capacitor: $C_{r1}, C_{r2}$</td>
</tr>
<tr>
<td>Output capacitor: $C_o$</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>TABLE III. EXPERIMENTAL COMPONENTS</th>
</tr>
</thead>
<tbody>
<tr>
<td>Name</td>
</tr>
<tr>
<td>-----------------------------------</td>
</tr>
<tr>
<td>Primary-side GaN-HEMT</td>
</tr>
<tr>
<td>Secondary-side GaN-HEMT</td>
</tr>
<tr>
<td>Gate Driver</td>
</tr>
<tr>
<td>Diode</td>
</tr>
<tr>
<td>Transformer Core Material</td>
</tr>
<tr>
<td>Resonant Capacitor</td>
</tr>
<tr>
<td>Input Capacitor</td>
</tr>
<tr>
<td>Output Capacitor</td>
</tr>
<tr>
<td>FPGA</td>
</tr>
<tr>
<td>Isolator</td>
</tr>
<tr>
<td>Current Transformer</td>
</tr>
</tbody>
</table>

Fig. 9. The prototype of the DC-DC converter.

<table>
<thead>
<tr>
<th>TABLE</th>
<th>Characteristics</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>$V_i$ = 42V</td>
<td>$D = 0.57 - 0.63$</td>
<td></td>
</tr>
<tr>
<td>$V_i$ = 45.5V</td>
<td>$D = 0.5 - 0.59$</td>
<td></td>
</tr>
</tbody>
</table>
B. The Performance of the Proposed Method

The open loop static characteristics of the 5MHz PWM-controlled DC-DC converter are exhibited as shown in Fig. 10. From the figure, it can be seen that $M$ is controlled by duty ratio $D$. The range of $M$ from 1.05 to 1.15 corresponds to the range of input voltage from 42 to 45.5V at $V_o = 12V$. Thus, by using the proposed PWM control technique, it can be found that the lower input voltage range limitation 42V of the target can be achieved. At $M = 1.05$, load current can be changed from 1 to 10A while maintaining $V_o = 12V$, when changing $D$ from 0.5 to 0.59. The difference between the targets and these results is due to the limitation of PWM and input voltage. In future work, the optimization of the turn ratio, resonant components and control technique will be discussed for expanding input voltage and load current range.

The waveforms of the proposed method are shown in Fig. 11. The magnetizing current $i_m$ are derived from resonant current $i_r$ and secondary-side current. The conditions of waveforms (a) and (b) are $M = 1.05$, $I_o = 10A$ and $M = 1.15$, $I_o = 6A$, respectively. From the drain-to-source voltage, ZVS of primary-side switches at turn-on seems to be achieved during dead time.

The power efficiency of the DC-DC converter is shown in Fig. 12. The maximum power efficiency is 89.2%. ZVS operation has been confirmed in the range of the experimental conditions.

The temperature distribution of the prototype DC-DC converter as shown in Fig. 13, has been taken at $M = 1.05$ and $I_o = 10A$. From the results, the temperature of the secondary-side is in high level, due to hard switching of the secondary-side switches. However, the power loss of the hard-switched switches in secondary-side is suppressed by using lower maximum voltage rating GaN-HEMTs of which $R_{ds(on)}$ is lower than that of primary-side GaN-HEMTs. Meanwhile, the primary-side temperature is relatively low because of achieving ZVS operation.

VI. CONCLUSIONS

In this paper, the principle of the proposed PWM-controlled current-mode resonant DC-DC step-down converter operated at MHz level switching frequency for high
power density is presented. By using the proposed method, the output voltage can be regulated without any additional components at fixed switching frequency. Furthermore, ZVS of primary-side switches at turn-on can be accomplished with the proposed phase-shift between primary and secondary-side switches.

The targets of the study is to obtain the high performances which is the small volume, 42-53V input voltage range, 10A maximum load current range, the realization of ZVS of primary-side switches at turn-on in all operating regions.

Some experiments have been performed with a 5MHz isolated DC-DC step-down converter which has GaN-HEMTs, and the total volume of the circuit is 16.14cm³. With the proposed PWM control method, input voltage range is 42-45.5V, and maximum load current range is 10A at 45.5V. The ZVS of primary-side switches at turn-on is confirmed, and the maximum power efficiency is 89.2%.

As the future work, to expand the input voltage range and the load current range with high power efficiency, the optimization of the turn ratio, resonant components and control technique will be discussed. Furthermore, feedback control which has high resolution by digital controller is under consideration.

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REFERENCES

Akinori Hariya (S‘13) was born in Nagasaki, Japan, in 1989. He received the B.S. degree in electrical engineering from Nagasaki University, Japan, in 2012. Since 2012, he belongs to Graduate school of Engineering, Nagasaki University. He is currently working toward the Ph.D. degree. He received Excellent Student Award of Kyushu Section, IEICE in 2012.

Ken Matsuura was born in Saitama Prefecture, Japan, in 1975. He received the B.S. and M.S. degrees in electrical engineering from The University of Tokyo, Japan, in 1998 and 2000, respectively. Since 2000, he has joined TDK Corporation. He is currently an engineer of Energy Devices Development Center Technology HQ. His main job is to develop the switched-mode power supply, especially DC-DC converters.

Hiroshige Yanagi was born in Nagasaki Prefecture, Japan, in 1985. He received the B.S. and M.S. degrees in electrical engineering from Nagasaki University, Japan, in 2007 and 2009, respectively. Since 2009, he has joined TDK-Lambda Corporation. His main job is to design the switched-mode power supply, especially DC-DC converters and PFC converters.

Satoshi Tomioka was born in Niigata Prefecture, Japan, in 1961. He received the B.S. degree in electrical engineering from Tokyo Denki University, Japan, in 1984, and the Ph. D. degree in electrical engineering from Kyushu University, Japan, in 2011. Since 1984, he has joined TDK-Lambda Corporation (formerly NEMIC-LAMBDA K.K.). He is currently a Senior manager of technology development department & power module development R&D division. His main job is research on switched-mode power supply, especially on DC-DC converters and PFC converters.

Yoichi Ishizuka (M‘94) received the B.S., M.S. degrees from Kagoshima University, Kagoshima, Japan in 1994, and the Ph.D. degree from Kumamoto University, Kumamoto, Japan in 1996. In 1996, he joined the department of electric and electronics of engineering, Faculty of Engineering, Nagasaki University, Nagasaki, Japan as research assistant. Since 2008, he has been an associate professor of the same department. His research activities include switched-mode power supply (SMPS), adaptive controller for SMPS and integrated analog power circuits.

Tamotsu Ninomiya (M'89-SM'98-F'01) received the B.E., M.E., and Dr.Eng. degrees in electronics from Kyushu University, Fukuoka, Japan, in 1967, 1969, and 1981, respectively. Since 1969 he had been associated with the Department of Electronics, Kyushu University, and since 1988 he had been Professor. In March 2008, he retired from Kyushu University, and moved to Nagasaki University, Japan as Professor of TDK endowed chair, and retired in 2013. He now serves as Visiting Research Professor at Asian Growth Research Institute in Kitakyushu city, Japan. He has been a specialist in the field of power electronics, including the analysis of switching power converters and their electromagnetic interference problems. He has published more than four hundred technical papers. He was Program Vice-Chairman for 1988 PESC, and General Chairman for 1998 PESC. For Japanese Institutes, he was awarded as IEICE Fellow in 2004, and as IEEJ Fellow in 2010.