



Title	DSP制御による情報通信用アクティブフィルタに関する研究
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## 参考文献

- (1) JIS Z8106 「音響用語」
- (2) 技術資料 「電源高調波電流について」 菊水電子工業編
- (3) Technical Notes 「低周波の EMC」 エヌエフ回路設計ブロック編
- (4) W.KOCZARA, M.RUKAT, R.TEOFILAK, L.GRZESIAK: “ACTIVE FILTER OPERATION PROVIDING UNITY POWER FACTOR SUPPLY SYSTEM FOR DRIVES AND BATTERY CHARGING”, IEEE Intelec1995, pp.219-222, Oct. 1995
- (5) A. van Zyl, J.H.R. Enslin, W.H.Steyn, R.Spee: “A New Unified Approach to Power Quality Management”, IEEE PESC1995, pp.183-188, June 1995
- (6) Chin-Yuan Hsu, Horng-Yuan Wu: “A New Single-Phase Active Power Filter with Reduced Energy Storage Capacitor”, IEEE PESC1995, pp.202-208, June 1995
- (7) V. Cardenas, N. Vazquez, C. Hernandez: “SLIDING MODE APPLIED TO A 3  $\Phi$  SHUNT ACTIVE POWER FILTER USING COMPENSATION WITH INSTANTANEOUS REACTIVE POWER THEORY”, IEEE PESC1998, pp.236-241, June 1998
- (8) 「UC3854 Application Manual」, Unitrode
- (9) Ying-Yu Tzou: “DSP-Based Fully Digital Control of a PWM DC-AC Converter for AC Voltage Regulation”, IEEE PESC1995, pp.138-144, June 1995
- (10) Dipl.-Ing., Heiko Osterholz: “Simple Fuzzy Control of a PWM Inverter for a UPS System”, IEEE Intelec1995, pp.565-570, Oct. 1995
- (11) Young-bok Byun, Ki-yeon Joe, Sung-jun Park, Cheul-u Kim: “DSP Control of Three-Phase Voltage Source UPS Inverter with Software Controlled Harmonic Conditioners”, IEEE Intelec1997, pp.195-200, Oct. 1997

- (12) Hisaaki Yamamoto, Hideo Iwamoto, Hiroshi Hattori and Mutsuo Nakaoka: "New Soft-Switching Inverter with Auxiliary Resonant Snubbers Using Pulse Current Feedback Transformer", IEEE Intelec1999, 26-3, Oct. 1999
- (13) 「DSP のすべて」日本テキサスインスツルメンツ（株）編
- (14) TMS320LF/LC240x シリーズセミナー資料 丸文株式会社編
- (15) Mario Cacciato, Alfio Consoli, Giuseppe Scarcella, Antonio Testa: "CONTINUOUS PWM TO SQUARE WAVE INVERTER CONTROL WITH LOW COMMON MODE EMISSIONS", IEEE PESC1998, pp.871-877, June 1998
- (16) Satoki Takizawa, Seiki Igarashi, Kazuo Kuroki: A New di/dt Control Gate Drive Circuit for IGBTs to reduce EMI Noise and Switching Losses", IEEE PESC1998, pp.1443-1449, June 1998
- (17) 庄山正仁, "スイッチング電源のコモンモードノイズ対策", 2001 スイッチング電源システムシンポジウム
- (18) 室山誠一, 松島敏雄, 村上直樹: "情報通信用電源システムの課題と動向", 信学論(B), vol.J84-B, no.5, pp.829-839, May 2001.
- (19) 池内貞広, 高橋光夫 "分散型電源設備の動向と省エネルギー対策", 総合設備コンサルタント, 技術年報, Vol.28.
- (20) 久留正敏, "地球温暖化防止と分散電源技術の進展", 日本技術士会, 機会部会, 4月定例講演, (Apr.2003)
- (21) 正田英介, 橋本英二, 坂下英二: "電源系統における高調波歪み規制と対策/測定技術", トリケップス, 1993.
- (22) 凌部光芳, 森川雅人: "スイッチング電源高調波歪み・対策と力率改善の動向", 日本能率協会スイッチング電源テクニカルフォーラム, pp.1-23, Nov. 1993.
- (23) 松尾博文, 黒川不二雄: "照明用電子安定器における入力電流高調波問題の研究", 照明学会研究調査報告書, JIER-063, May 1999.
- (24) S. Ofuji, T. Oshikata and Y. Matsuda: "New switching sequence for three phase converter using a transformer", IEE/Japan Power Conversion Conf., pp.797-800, Aug. 1997.

- (25) 「デジタル信号処理」, 電子情報通信学会編, コロナ社, 1987.
- (26) R. D. Middlebrook and S. Cuk: "A general unified approach to modeling switching converter power stages", IEEE PESC1976, pp.18-34, June 1976.
- (27) F. Fallside and A. R. Farmer: "Ripple instability in closed-loop control systems with thyristor amplifiers", IEE, pp. 139-152, Jan. 1967.
- (28) E. J. Miller: "The use of resonant circuits in power conditioning equipment", IEEE PESC1971, pp.94-100, June 1971.
- (29) R. Redl, B. Molner and N. O. Sokal: "Class E resonant DC-DC power converters analysis of operation and experimental result at 1.5MHz", IEEE PESC1983, pp.50-60, June 1983.
- (30) Wojciech A. TABISZ and Fred C. LEE: "ZERO-VOLTAGE-SWITCHING MULTI-RESONANT TECHNIQUE A NOVEL APPROACH TO IMPROVE PERFORMANCE OF HIGH-FREQUENCY QUASI-RESONANT CONVERTERS", IEEE PESC1988, pp.9-17, April 1988.
- (31) Satoshi Hamada and Matsuo Nakaoka: "A Novel Zero-Voltage and Zero-Current Switching PWM DC-DC Converter With Reduced Conduction Losses", IEEE Transactions on Power Electronics, Vol.17,NO.3, May 2002
- (32) Hiroyuki HAGA, Kiyoshi OZU, Yoshihiko KIKUCHI: "ZERO VOLTAGE SOFT SWITCHING CONVERTER WITH HIGH INPUT VOLTAGE", IEEE Intelec1991, pp.648-654, Nov. 1991
- (33) Kazurou Harada, Yoshiyuki Ishihara and Toshiyuki Todaka: "Analysis and Design of ZVS-PWM Half-Bridge Converter with Secondary Switches", IEEE PESC1995, pp.280-285, April 1995.
- (34) Domingo Ruiz-Caballero and Ivo Barbi: "A NEW ZVS-PWM CLAMPING MODE ISOLATED NON PULSATING INPUT AND OUTPUT CURRENT DC-TO-DC CONVERTER", IEEE Intelec1999, 20-1, Nov. 1999

- (35) Rajapandian Ayyanar and Ned Mohan: “Full-Load-Range-ZVS Hybrid DC-DC Converter with Two Full-Bridges for High-Power Battery Charging”, IEEE Intelec1999, 20-2, Nov. 1999
- (36) 「高周波化・軽薄短小化におけるスイッチング電源と事例」, ミマツデータシステム社
- (37) Richard Zhang, Fred C. Lee, Dushan Boroyevich, and Hengchun Mao: “New High Power, High Performance Power Converter Systems”, IEEE PESC1998, pp.8-14, April 1998.
- (38) Lidong Zhang, Math H J Bollen: “A Method for Characterisation of Three-Phase Unbalanced Dips(Sags) from Recorded Voltage Waveshapes”, IEEE Intelec1999, 13-2, Nov. 1999
- (39) Y.S.Sun, S.H.Woo, C.H.Kang, J.S.Yoo,H.J.Kim: “A Development of the Large Capacity Telecommunications Rectifier System Using Series-Resonant Technology and Half-Bridge Topology”, IEEE Intelec1999, 20-2, Nov. 1999
- (40) 「TMS320F24x Controllers」, TEXAS INSTRUMENTS
- (41) 「パワーエレクトロニクス回路」, 電気学会・半導体電力変換システム調査専門委員会編 オーム社
- (42) 「DSP 活用のポイント」, 石田義久, 鎌田弘之 産業図書
- (43) 「フィルタ回路入門」, 三谷政昭訳, 森北出版株式会社